

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2814

Examiner: Ms. Theresa T. Doan

In Re PATENT APPLICATION of:

Applicant(s): Hirotaka KOMATSUBARA

Serial No.: 10/812,959

Filing date: March 31, 2004

For: SEMICONDUCTOR DEVICE WITH

REDUCED INTERCONNECT.

CAPACITANCE

Atty. ref.: MAE 309

<u>AMENDMENT</u>

January 23, 2006

Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Madam:

In response to the Examiner's Action mailed on August 23, 2005, the period for response to which is extended by a Petition for a Two-month Extension of Time filed herewith, please amend the above-identified application as follows: